(Computer Organization)

A- Basic Information

1. **Title and Code**

   Computer Organization/
   CS312

2. **Programme(s) on which this course is given**   Computer Science

3. **Academic year / Level of Programme**   3rd year / 1st semester

4. **Units/Weekly hours**

   Lecture 3   Tutorial/Practical 3   Total 6

5. **Names of lecturers contributing to the delivery of the course**
   - Dr. Mohammed Shoala

   Course co-ordinator : Dr. Mohammed Shoala

   External evaluator :   Not assigned yet.

B- Statistical Information

No. of students attending the course: No. 109 % 100

No. of students completing the course: No. 109 % 100

Results:

Passed: No. 104 % 95   Failed: No. 5 % 5

Grading of successful students:

Excellent: No. 10 % 9   Very Good: No. 21 % 19

Good :   No. 22 % 20   Pass: No. 51 % 47

C- Professional Information

1 – Course Teaching

Topics actually taught	No. of hours	Lecturer
1 Introduction	3	**Dr. Mohammed Shoala**
2 Computer Operations • Introduction. • Register transfer and Microoperations. • Bus transfer and Processor unit. • Arithmetic, logic, and shifter units. • Control word.	12	**Dr. Mohammed Shoala**

3 Control Logic Design • Introduction. • Microprogrammeed control. • Processor unit control. • Design of simple computer.	15	**Dr. Mohammed Shoala**
4 Computer Instructions • Introduction. • Address field. • Addressing modes. • Stack organization. • Data transfer instructions. • Data manipulation instructions. • Floating point operations. • Programme control instructions. • Programme interrupt.	12	**Dr. Mohammed Shoala**
5 CPU Design • Introduction. • Arithmetic logic shift unit. • Processor unit. • Instruction and microinstruction formats. • Microprogramme for computer cycle. • Microprogramme routine. • Control unit.	18	**Dr. Mohammed Shoala**
6 Memory Organization • Memory hierarchy. • Special types of memory. • Magnetic and optical memories. • Associative memories. • Cache memory. • Virtual memory. • Memory management hardware.	12	**Dr. Mohammed Shoala**
7 I/O Organization • Introduction. • Parallel interfacing. • CPU-initiated data transfer. • Interfacing data converters. • I/O-initiated data transfer. • Serial I/O.	12	**Dr. Mohammed Shoala**

Topics taught as a percentage of the content specified:

>90 % [√]   **70-90 %** [ ]   **<70%** [ ]

2- Teaching and Learning Methods:

Lectures: [√]

Practical Training/ Laboratory: [√]

Seminar/Workshop: [√]

Class Activity: [√]

Case Study:

Other Assignments/Homework: √

3- Student Assessment:

Method of Assessment	Percentage of total
Written examination	70
Oral examination	10
Practical/laboratory work	-
Other Assignments/class work	20
Total	100 %

Members of Examination Committee
Dr. Mohammed Shoala
Mr. Abd El_Aleem Kamal

Role of external evaluator:
External evaluator not assigned yet.

4- Facilities and Teaching Materials:

Totally adequate ☐

Adequate to some extent √

Inadequate ☐

5- Administrative Constraints

- Insufficient Class rooms and Halls.
- There are no labs for this course.

6- Student Evaluation of the course: Response of Course Team

-Need more Case studies.   -Increase the case study .
-Need practical exercises.   -Add more practical exercises.

7- Comments from external evaluator(s):
External evaluator not assigned yet.

8- Course Enhancement:
Progress on actions identified in the previous year's action plan:
This is the first year and no previous action Plan**.**

Course Coordinator: Dr. Mohammed Shoala

Signature:

Date: